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# PERFORMANCE ANALYSIS AND SIMULATION OF A GRID CONNECTED THREE-PHASE INDUCTION GENERATOR

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## ABSTRACT:

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This paper aims to study the performance of a three-phase Induction Generator (IG) connected to the power utility grid. For this study, a complete analysis and simulation of a small wind power system (SWPS) based on direct modeling techniques has been presented. A theoretical analysis is reported to clarifying the system under consideration. A mathematical model associated with each component in the (SWPS) is developed. The method of interconnection has also been discussed. Finally, a finite number of disturbances associated with such wind power systems are introduced and a demonstration results such as terminal voltage, stator current, power, and speed at different modes of operation are depicted and discussed.

في العنوات الأخيرة تم الاتجاه إلى توليد الطاقة الكهربائية بواسطة المصادر البديلة مثل طاقة الرياح والطاقة الشَّمسية، والسبب يعود إلى توفر هذه الطاقات في اغلب دول العالم ورخص تمنُّها إضافة إلى أن هذه الطاقات تعتبر صديقة للبيئة. عادة ما تستخدم المولدات التأثيرية في توليد الطاقة الكهربانية سواء في حالة التغذية الذاتية أو في حالة ربطها مع الشبكة.

يهدف هذا البحث آلى دراسة سلوك مولد تأثيري ثلاثي الأوجه مربوط إلى الشبكة العامة. تم عمل دراسة و تحليل و نمذجة للمولد والشبكة وذلك باستخدام ثقنية التمثيل المباشر. تمت دراسة أداء المولد تحت ظروف تشغيل مختلفة مَنَّل حالة قصرُ ثَلَّائي الأوّجه على أطرأف المولد، تغيير عزّم الدخل، و تغيير مفاجئ في عزم الدخل للمولد. تم عرض وتحليل النتائج اللازمة للحالات التشغيلية السابقة ومنها عرض الجهد على إطراف المولد ، العزم

الكهرومغناطيسي، قدرة الدخل، سرعة المولد، وتيار المولد.

من خلال النتائج التي تم الحصول عليها في هذا البحث وجد أن هنالك تطابق مع النتائج التي تم الحصول عليها من قبل باحتين أخرين في نفس المجال.

Induction Generator (IG), Small Wind Power System (SWPS), Performance, Keywords: Simulation.

#### **1. INTRODUCTION**

Induction Generators (IGs) have been found to be very attractive solution for alternative renewable energy resources. In contrast to other conventional alternators, induction generators have certain inherent advantages such as, low unit cost, less maintenance, brushless ruggedness, rotor construction (squirrel cage type), high reliability during transient situations, and self protection against over loads and short circuits [1-4].

When an appropriate capacitor bank is connected across the stator terminals of an induction machine and the rotor is driven by a prime mover, the phenomena of self-excitation will exist. Under such circumstances the terminal capacitance tends to supply the necessary lagging VARs for establishing the airgap flux and the machine is referred to as selfexcited induction generator (SEIG) [5].

Unlike the (SEIG), the frequency of the grid connected induction generator is locked to the

frequency of the power utility grid but the speed may vary [6]. Capacitor banks are connected to the generator terminals to improve the system power factor and also to reduce the VARs drain from the grid to which the generator is connected. The effect of capacitors size will have to be appraised to arrive at appropriate rating.

This paper is organized as follows: Section I gives an introduction. Section II introduces the system to be investigated. Section III focused on the main components of the system under study, finally demonstrated results are presented and some specific conclusions are drawn for this paper.

#### 2. SYSTEM UNDER STUDY

The system to be investigated is shown in Fig. 1. As shown, this system comprises the following an induction generator, power components: transformer, capacitor bank, static load, and short transmission line (all are three-phase). A

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mathematical model associated to each component is given below.

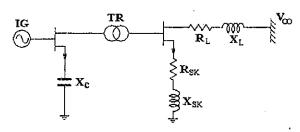


Fig. 1 Representation of a small wind power system

#### 3. POWER SYSTEM COMPONENTS MODELING

In this section, only the power system components which associated with small wind power system are described.

#### 3.1. The induction generator

The basic equivalent circuit of a three phase induction generator may be expressed as follows:

• Flux Linkage Equations:

In matrix notations, the flux linkages of the stator and rotor windings, in terms of the winding inductances and currents, may be written compactly as given in (1), where subscripts s and r denotes to stator and rotor windings respectively [7].

$$\begin{bmatrix} \lambda_s \\ \lambda_r \end{bmatrix} = \begin{bmatrix} L_s L_{sr} \\ L_{rs} L_r \end{bmatrix} \begin{bmatrix} I_s \\ I_r \end{bmatrix}$$
(1)

Where

252

$$[\lambda_s] = \begin{bmatrix} \lambda_{as} \lambda_{bs} \lambda_{cs} \end{bmatrix}^T$$
(2)

$$[\lambda_r] = \left[ \lambda_{ar} \lambda_{br} \lambda_{cr} \right]^T \tag{3}$$

$$[I_s] = \begin{bmatrix} I_{as} I_{bs} I_{cs} \end{bmatrix}^T \tag{4}$$

$$[I_r] = \begin{bmatrix} I_{ar} I_{br} I_{cr} \end{bmatrix}^T$$
<sup>(5)</sup>

$$\begin{bmatrix} L_{s} \end{bmatrix} = \begin{bmatrix} (L_{ts} + L_{ms}) & -0.5L_{tss} & -0.5L_{ms} \\ -0.5L_{ms} & (L_{ts} + L_{ms}) & -0.5L_{ms} \\ -0.5L_{ms} & -0.5L_{ms} & (L_{ts} + L_{ms}) \end{bmatrix}$$
(6)

$$\begin{bmatrix} L_r \end{bmatrix} = \begin{bmatrix} (L_{tr} + L_{mr}) & -0.5L_{mr} & -0.5L_{mr} \\ -0.5L_{mr} & (L_{tr} + L_{mr}) & -0.5L_{mr} \\ -0.5L_{mr} & -0.5L_{mr} & (L_{tr} + L_{mr}) \end{bmatrix}$$
(7)

$$L_{ur}^{abc} = L_{rr} \begin{bmatrix} \cos\theta_{r} & \cos\left(\theta_{r} + \frac{2\pi}{3}\right) & \cos\left(\theta_{r} - \frac{2\pi}{3}\right) \\ \cos\left(\theta_{r} - \frac{2\pi}{3}\right) & \cos\theta_{r} & \cos\left(\theta_{r} + \frac{2\pi}{3}\right) \\ \cos\left(\theta_{r} + \frac{2\pi}{3}\right) & \cos\left(\theta_{r} - \frac{2\pi}{3}\right) & \cos\theta_{r} \end{bmatrix}$$
(8)

Lls and Lms are respectively the leakage and magnetizing inductance of the stator windings. Llr and Lmr are for the rotor windings. The inductance Lsr is the amplitude of the mutual inductance between stator and rotor windings.

• Voltage Equations:

Using the coupled circuit approach and generator notation, the voltage equations of the magnetically coupled stator and rotor circuits can be written as:

$$\begin{bmatrix} v_s \\ v_r \end{bmatrix} = p \begin{bmatrix} \lambda_s \\ \lambda_r \end{bmatrix} + \begin{bmatrix} R_s \\ R_r \end{bmatrix} \begin{bmatrix} I_s \\ I_r \end{bmatrix}$$
(9)

Where:

$$[v_s] = \left[ v_{as} v_{bs} v_{cs} \right]^T \tag{10}$$

$$[v_r] = \left[ v_{ar} v_{br} v_{cr} \right]^T \tag{11}$$

$$[R_{s}] = diag[R_{as}R_{bs}R_{cs}]$$
<sup>(12)</sup>

$$[R_r] = diag[R_{ar}R_{br}R_{cr}]$$
<sup>(13)</sup>

Substituting for  $P[\lambda s]$  and  $P[\lambda r]$  by the equation (1) and using the matrix [G] notation. Equation (9) can be written in partitioned form as:

 $[Vs] = [Rs][Is]+[Ls]P[Is]+[Lsr]P[Ir]+\omega r[Gsr][Ir]$ (14.a)

 $[Vr]=[Rr][Ir]+[Lr]P[Ir]+[Lsr]P[Is]+\omega r[Grs][Is]$  (14.b) Where:

$$[L_{ss}] = [L_{ss}]^T \tag{15.a}$$

$$[G_n] = [G_{sr}]^T \tag{15.b}$$

$$[G_s] = [G_r] = 0 \tag{15.c}$$

A matrix [G] is introduced to represent the rotation performance of the electrical machine and is called the *rotational inductance matrix*. P[L] is defined as  $\omega r$ [G] where  $\omega r$  is the rotor speed in electrical radians/sec. and P is the differential operator.

## Torque Equations:

The torque equation of the induction generator in terms of stator equations can be expressed as:

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$$T_{c} = \frac{2}{3\sqrt{3}}\omega_{s} \left(\lambda_{s} \left(i_{bs} - i_{cs}\right) + \lambda_{bs} \left(i_{cs} - i_{as}\right) + \lambda_{cs} \left(i_{as} - i_{bs}\right)\right)$$
(16)

where ws is the synchronous speed in electrical radians/sec.

Also the mechanical equation of motion may be expressed as:

(17) Por=(Tin-Te)/2H

Where Tin is the input torque and H is the inertia constant for the induction machine.

#### 3.2. The power transformer

The voltage equations of a three phase power transformer may expressed as:

$$[V] = P[\lambda] + [R][I]$$
(18)

$$[\lambda] = [L][I] \tag{19}$$

$$\begin{bmatrix} \lambda \end{bmatrix} = \begin{bmatrix} \lambda_{a1} \lambda_{b1} \lambda_{c1} \lambda_{a2} \lambda_{b2} \lambda_{c2} \end{bmatrix}^T$$
(20)

$$\begin{bmatrix} v \end{bmatrix} = \begin{bmatrix} v_{a1}v_{b1}v_{c1}v_{a2}v_{b2}v_{c2} \end{bmatrix}^T$$
(21)

$$[I] = [I_{a1}I_{b1}I_{c1}I_{a2}I_{b2}I_{c2}]^{T}$$
(22)

$$[R] = diag[R_{a1}R_{b1}R_{c1}R_{a2}R_{b2}R_{c2}]$$
<sup>(23)</sup>

$$\begin{bmatrix} L \end{bmatrix} = \begin{bmatrix} L_{a1a1} & L_{a1b1} & L_{a1c1} & L_{a1a2} & L_{a1b2} & L_{a1c2} \\ L_{b1a1} & L_{b1b1} & L_{b1c1} & L_{b1a2} & L_{b1b2} & L_{b1c2} \\ L_{c1a1} & L_{c1b1} & L_{c2c1} & L_{c1a2} & L_{c1b1} & L_{c1c2} \\ L_{a2a1} & L_{a2b1} & L_{a2c1} & L_{a2a2} & L_{a2b2} & L_{a2c2} \\ L_{b2a1} & L_{b2b1} & L_{b2c1} & L_{b2a2} & L_{b2b2} & L_{b2c2} \\ L_{c2a1} & L_{c2b1} & L_{c2c1} & L_{c2a2} & L_{c2b2} \end{bmatrix}$$

$$(24)$$

Where, subscripts 1 and 2 denote the primary and secondary quantities respectively.

#### 3.3. Static load

The lumped static load can be modeled as a resistive load Rk and a reactive load Lk. The voltage equation for the static loads may be expressed as:

$$[Vsk] = [Rsk][Isk] + [Lsk]P[Isk]$$
(25)

Where,

$$[v_{sk}] = \begin{bmatrix} v_{ak} v_{bk} v_{ck} \end{bmatrix}^T$$
(26)

$$\begin{bmatrix} I_{sk} \end{bmatrix} = \begin{bmatrix} I_{ak} I_{bk} I_{ck} \end{bmatrix}^T$$
<sup>(27)</sup>

$$\left[R_{sk}\right] = diag \left[R_{ak}R_{bk}R_{ck}\right]^{T}$$

$$\begin{bmatrix} L_{sk} \end{bmatrix} = diag \begin{bmatrix} L_{ak} L_{bk} L_{ck} \end{bmatrix}^T$$
<sup>(29)</sup>

# 3.4. The power factor correction capacitor

The lumped capacitor model can be expressed by a set of differential equations as:

$$[sc] = [Csc]P[Vsc]$$
(30)

Where,

$$\begin{bmatrix} I_{sc} \end{bmatrix} = \begin{bmatrix} I_{ac} I_{bc} I_{cc} \end{bmatrix}^T$$
(31)

$$[v_{sc}] = \begin{bmatrix} v_{ac} v_{bc} v_{cc} \end{bmatrix}^T$$
(32)

$$\begin{bmatrix} C_{sc} \end{bmatrix} = diag \begin{bmatrix} C_{ae} C_{be} C_{ce} \end{bmatrix}^T$$
(33)

#### 3.5. Short transmission line

The short transmission line can be modeled similar to the static load that described in section C.

# 4. METHOD OF INTERCONNECTION

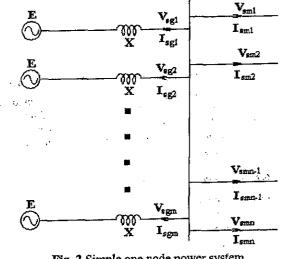
The method of interconnection is base on applying the Kirchhoff's current and voltage laws at each node in the system, e.g. consider the simple one node system shown in Fig. 2, which contains m generators and n load.

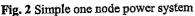
Kirchhoff's current law:

$$\sum_{k=1}^{m} I_{skg} + \sum_{\ell=1}^{n} I_{sme} = 0$$
(34)

and Kirchhoff's voltage law may be stated as:

Vsg1=Vsg2=....=Vsgm=Vsm1=Vsm2=....=Vsmn (35)





Engineering Research Journal, Minoufiya University, Vol. 31, No. 3, July 2008

(28)

253

#### 5. REPRESENTATION OF A SMALL WIND POWER SYSTEM

The application of the method of interconnection is illustrated by considering the simple system shown in Fig. 1, which comprises a three phase induction generator, power transformer, capacitor bank, static load, short line, and infinite busbar. The system can be expressed by a set of first-order differential equations of the form:

$$[V] = [R][I] + [L]P[I] + [G][I]$$
 (36)

Where,

$$\begin{bmatrix} v \end{bmatrix} = \begin{bmatrix} v_{rg}, 0, 0, 0, v_{\infty}, 0 \end{bmatrix}^{T}$$
(37)

$$\begin{bmatrix} I \end{bmatrix} = \begin{bmatrix} I_{rg}, I_{1T}, I_{sc}, I_{sc} I_{L} I_{sc} \end{bmatrix}^{T}$$
(38)

[R],[L]and [G] are square matrices and can be found as:

# 6. SYSTEM DISTURBANCES AND RESULTS

A sequence of simulation studies were conducted on the system shown in Fig.1. The procedures for applying the various types of disturbances to the system under consideration are represented in the following tasks.

#### 6.1. Balanced three-phase fault:

In the simulation of balanced three-phase fault only the static impedances are replaced by fault impedances, that is:

Rask=Rbsk=Rcsk=R <sub>f</sub>	(42)
Rask=Rbsk=Rcsk=R <sub>f</sub>	(42)

Lask=Lbsk=Lcsk=L<sub>f</sub> (43)

Where  $R_f$  and  $L_f$  are the resistance and inductance of the fault impedance.

Generator synchronized to the system with (0.9 p.u.) constant torque and three phase balanced fault applied at the transformer HV winding after two seconds. After 200ms the fault is cleared.

Figures 3, 4, 5, 6, and 7 show waveforms rotor speed, electromagnetic power, power input, average terminal voltage, and phase A current respectively.

#### 6.2. Cycling in input torque:

A 25% of the rated input torque (0.9 p.u.) as a cycling torque superimposed on the (0.75) of the rated constant input torque. The equation of the input torque for 1 Hz frequency of cycling can be expressed as:

$$Tin = (0.75*0.9) + (0.25*0.9) \sin (2\pi T)$$
(44)

Selected results of the study are given in figures 8, 9, 10, 11, and 12.

#### 6.3. Step change in input torque:

The input torque is made equal to (0.25p.u.) for the first two seconds of the simulation and the input torque is increased to (0.9p.u.) and kept constant for the rest of the simulation time.

Selected results represent rotor speed, electromagnetic power, power input, average terminal voltage, and phase A current are, respectively given in figures 13, 14, 15, 16, and 17.

#### 7. CONCLUSIONS

In this paper a study, modeling, and analysis of a small wind power system has been presented. The system considered in this paper consists of an induction generator, power transformer, short transmission line, static load, and a power factor correction capacitor. In order to examine the mentioned system, a multi modes of operation and disturbances; which are practically sudden; are applied such as three phase short circuit, cycling in input torque, and step change in input toque, these modes are simulated and good correlations to the practical situations are depicted. The simulation and fault duration used in the research respectively; are 5 sec, and 200 msec., which are really recommended time for the most authors in the field.

#### ACKNOWLEDGEMENT

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254

M Nasr, S. El-Saiah, "Performance Analysis and Simulation of a Grid Connected Three-Phase Induction Generator"

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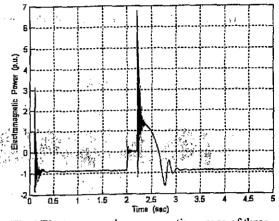
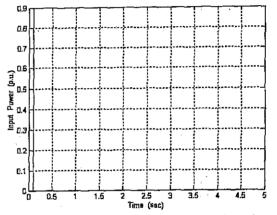
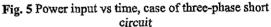


Fig.4 Electromagnetic power vs time, case of threephase short circuit





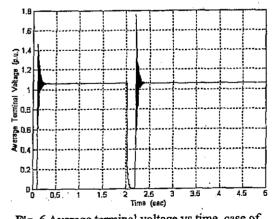
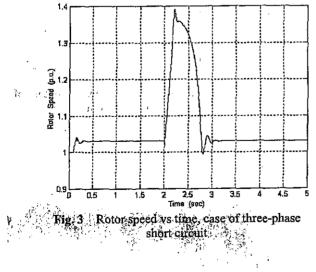


Fig. 6 Average terminal voltage vs time, case of three-phase short circuit



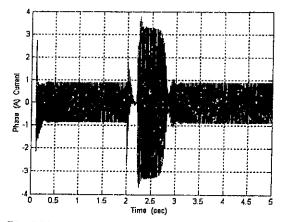


Fig. 7 Phase (A) current vs time, case of three-phase short circuit

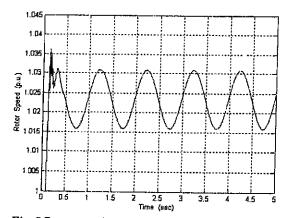
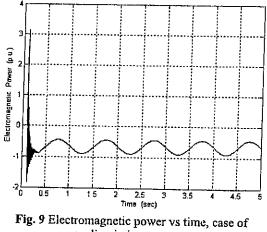
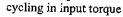


Fig. 8 Rotor speed vs time, case of cycling in input torque





256

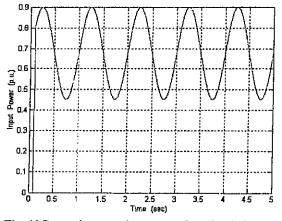


Fig. 10 Power input vs time, case of cycling in input torque

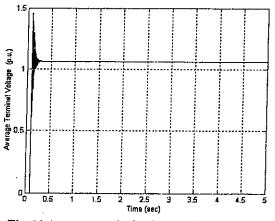


Fig. 11 Average terminal voltage vs time, case of cycling in input torque

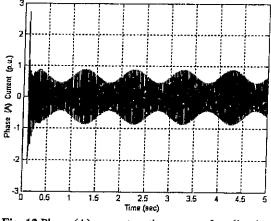
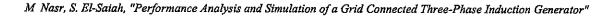


Fig. 12 Phase (A) current vs time, case of cycling in input torque



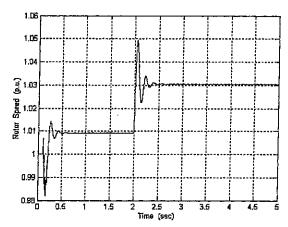
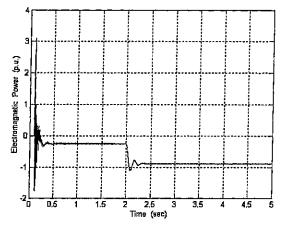
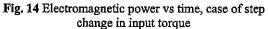
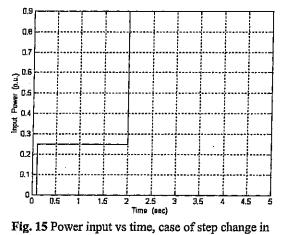


Fig. 13 Rotor speed vs time, case of step change in input torque







input torque

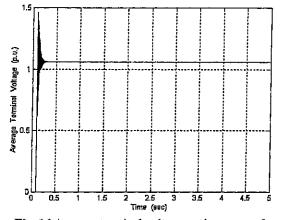
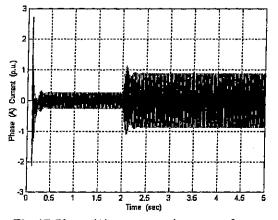
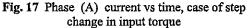
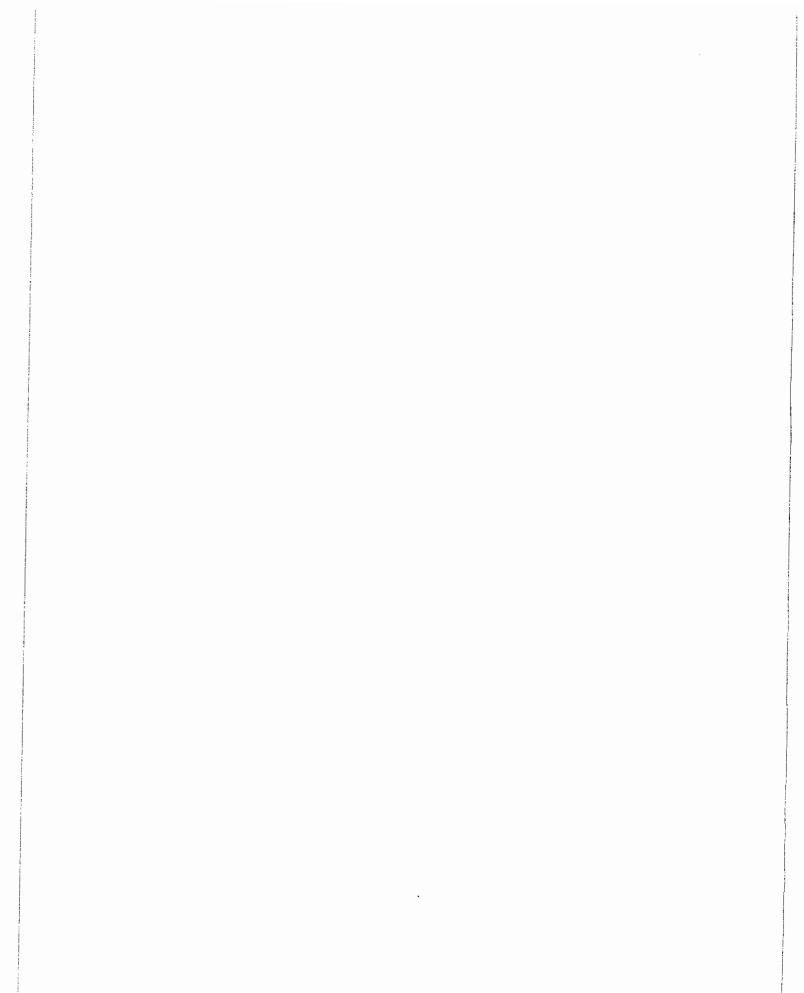


Fig. 16 Average terminal voltage vs time, case of step change in input torque







S. Al-Ghamdi, "Fingerprint Recognition Based Upon A Data Analysis Input Mapping for a pRAM Neural Network"

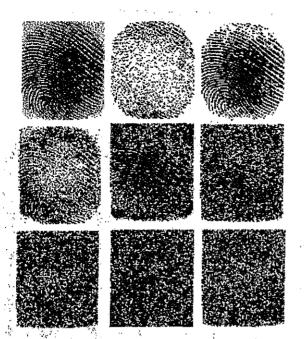


Figure 3 Multi-grey level image and its 8 binary planes.

This method called the bit plane decomposition technique decodes a given pixel value within the original image using the following equation.

$$X_{ij}=2^{N-1} x_{(N-1)ij} + + 2^{p} x_{pij} + + x_{0ij}$$
(6)

where  $x_{(N-1) ij}$ ,  $x_{pij}$ ,  $x_{0ij}$  are either 0 or 1 and represent the pixel values corresponding respectively to the N binary planes at position (i,j) for all  $i,j \in \{0, ..., 511\}$ (case of 512 by 512 image resolution) and  $N=\log_2 M$ , M being the number of grey-levels.

#### 4.2 High Resolution Images

In general, increasing the image resolution requires larger demand on the memory capacity. To overcome this limitation, we split the image into a set of non-overlapping sub-images. Then we dedicate one pyramidal net to handle each sub-image. This allowed us, in the case of a classification problem, to define what we called the *convergence map* that was used to discard the irrelevant sub pyramids (i.e. do not converge) and build the whole pyramid using only the output of sub-pyramids that converged, reducing the memory storage required for a high resolution image with the added advantage of being able to train all sub-pyramids in parallel if necessary. The tests were performed on a four pattern classification problem convergence map highlighting the parts of the images which are totally similar (grey parts), totally different (low values) or in between (values greater than 0.1).

## 5. THE PROPOSED SAMPLING SCHEME

The manner with which the n-pixels are extracted to form the input to the pRAM node is referred to the input sampling or mapping. These combined pixels form the sub-pattern, called *n-tuple* state, generally used as an address to a single pRAM, to decode the corresponding *n-tuple* state. we propose a new input mapping based on data analysis as we believe that extracting knowledge from the actual data to select the n-tuples is the most appropriate way to extract the salient features of the image.

In order to obtain the input mapping in the case of a P pattern recognition problem, we consider the local variations seen by each pixel within the image according to the data used. As a result, we get L (image size in pixels) functions each of which can be represented by P (number of patterns) strings of length T (size of each training set). Since each individual pixel goes through a cycle of variations, we suggest to compute the resultant probability of a given n-tuple to be in a given state based on the individual probability densities of the constituent pixels. Hence, we use the frequency of occurrence of a pixel to be in the state 1, as the main metric to represent changes at the pixel level while handling one training set at a time. Normally, if a n-tuple state is a characteristic one for a given training set, this state should be the pixel level while handling one training set at a time. Normally, if a n-tuple state is a characteristic one for a given training set, this state should be the frequency of occurrence of a pixel to be in the state 1, as the main metric to represent changes at the pixel level while handling one training set at a time. Normally, if an n-tuple state is a characteristic one for a given training set, this state should be approximately constant (i.e. occurs with a high probability) and that particular state must have a low probability to occur for other training sets. Hence, to group pixels in n-tuples we must test for their frequency of occurrence in a particular state.

## 6. FINGERPRINT RECOGNITION

Fingerprint recognition is one of the most successful visual biometric applications which can be carried out by having a fingerprint scanned. The data from the scan is converted to a unique template, encrypted, and stored as numerical data. The recognition requires the individual to present his fingerprint for comparison with the previously recorded data. A fingerprint recognition scheme looks at the patterns found on a fingertip, for instance, we can use certain distinguishing features such as the ridges and valleys found on the surface tips of a human finger to identify an individual. The problem that one can face is the feature extraction stage on which we base the recognition. We propose, as an alternative to classical

techniques, the use of a pRAM neural net with a judiciously selected data based input mapping to overcome the problem of feature extraction stage.

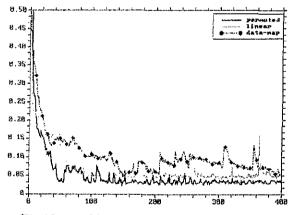
The fingerprint is 192x128 multi-grey level image, so we convert it to binary images using the previously stated method and we divide the resultant images into small shrinks each of which has a size of 24x16, as shown in Figure 3.

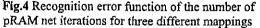
### 7. TESTS AND RESULTS

A number of computer simulations were carried out to highlight the effect of using different input mappings on a pyramidal neural network to perform recognition of 24x16 binary images. Three input mappings were tested, namely the structured (i.e. regularly samples n consecutive pixels: linear), the database was derived using the proposed algorithm given in the previous section, and a permuted version of the latter. The spatial distributions of the n-tuples represented by these mappings are respectively indicated using the joint n-tuple distance map given in Fig 5.

This figure also summarizes the proportion of each ntuple type obtained from a corresponding n-tuple input mapping. The performance is measured using the convergence and recognition rates, as these are the most common metrics used for pattern recognition problems. The obtained results are illustrated in Fig.4.

Figure 4 shows that the network's convergence is better in the case of the permuted and data-based mappings when compared to linear mapping. Using the permuted mapping we managed to achieve low convergence error rate approximated at 2% compared to 13% for the structured mapping and 6% in the case of the data-based one without permutation.





The fluctuations seen are the result of changes in the pRAM memory contents which vary in the range

[0,1]. The network using the permuted mapping achieved the highest recognition rate of 93%, whereas the non-permuted mapping only achieved 80% even worse than that of the structured input mapping. This result can be justified by consulting the JND map.

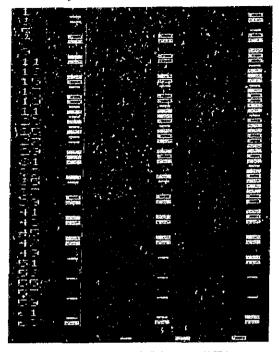


Fig. 5. The Joint n-tuple Distance (JND) map

Indeed, we see that the structured mapping presents relatively good distribution of all n-tuple types throughout the input layer of the pyramidal net compared to the data-based input mapping In rearranging the n-tuples obtained, using the databased algorithm, we obtain the *permuted mapping*, which presents better spatial distribution of the ntuples compared to the original data-based one. This result allows us to state that breeding a good input mapping is not sufficient when used with a *p*RAM pyramidal network, we must, in addition, provide a uniform distribution of the n-tuple types at the network input.

The bit plane encoding scheme does not assume any underlying constraints on the images and allows the use of the same pRAM net to exercise different types of data i.e. the binary planes corresponding to each grey scale image, which makes the computer simulation much more easier. As a result, the obtained pRAM net was extensively exercised to handle successive fingerprint images using the transformations stated above which required a total of 512 pRAM nets in order to perform recognition of fingerprints. However, we were faced with some

262

S. Al-Ghamdi, "Fingerprint Recognition Based Upon A Data Analysis Input Mapping for a pRAM Neural Network"

redundancy in the obtained binary parts, the reason why we used the convergence map to reduce the number of pRAM nets considered in our simulation. The work is still going on, and the obtained results will be published in the near future.

# 8. CONCLUSION AND FURTHER WORK

In this paper pRAM nets were simulated and used to tackle an image recognition problem. This study emphasized the benefit in using a pRAM net with a two stage input mapping based on data analysis. The extracts n-tuples according to first stage discrimination among patterns at the n-tuple level. This delivers a mixture of n-tuple types and when used with a single pyramidal pRAM net without any overlap among n-tuples, it required an adequate distribution of the obtained n-tuples at the pyramid base. The application of these propositions to binary image recognition was successful and confirmed their effectiveness when handling a pattern recognition task. The obtained results are very motivating and encouraging. This work is still under investigation in order to set up a complete automatic system to handle recognition of fingerprints. We are also investigating the implementation of a dynamic reconfigurable pRAM net with a complete test bench software based on Java programming so that we can apply multithreading to use several nets simulator and run them in parallel. This will certainly improve the processing speed of the recognition task handled. The use of more than one pyramid, so as to allow for the overlap among the n-tuples, plays a positive role in enhancing the performance. Compression can be performed using state of the art techniques to the

input image instead of applying it as it is, in a raw format, to the input of the pRAM.

These suggestions are left as a possible continuation to this work. However, we should bear in mind that this application is meant to be a tool to verify the adequacy of the proposed approach to select adequate n-tuples and the effect of distributing them at the input of a Boolean neural network. We did not intend to specifically solve the fingerprint recognition problem, this is the reason why no complete comparative study was undertaken with other techniques in this paper. Nevertheless, the good results obtained are very motivating and suggest that the use of such a scheme should be further investigated.

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